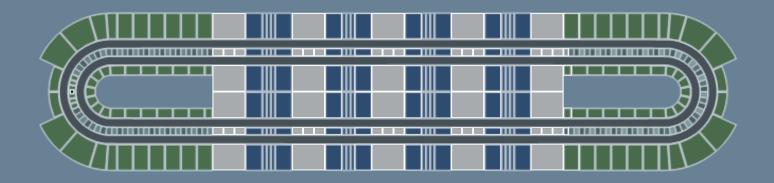


# Benchmarking the Quantinuum Stack Daniel Mills



# TKET

# INQUANTOQUANTUM ORIGIN

LAMBEQ QERMIT

### H-Series at-a-glance

System		Qubits	2Q gate infidelity (10 <sup>-3</sup> )	QV (log <sub>2</sub> )
HØ	(retired)	$4 \rightarrow 6$	8.0	$4 \rightarrow 6$
H1		10 → 20	5.0 → 1.3	7 → 19
H2		32 → >50*	$1.8 \rightarrow <1.3^*$ *Planned upgrades	16 → >19*

Other specification data: https://github.com/CQCL/quantinuum-hardware-specifications Quantum volume data: https://github.com/CQCL/quantinuum-hardware-quantum-volume

## **Benchmarking tests**

#### Component

- Single-qubit randomized benchmarking
- Two-qubit randomized benchmarking
- Two-qubit SU(4) randomized benchmarking
- Two-qubit parameterized gate randomized benchmarking
- Measurement crosstalk bright state depumping
- Reset crosstalk bright state depumping
- SPAM test
- Two-qubit cycle benchmarking

#### System-level

- Mirror benchmarking
- Quantum volume
- Random circuit sampling
- GHZ state fidelity

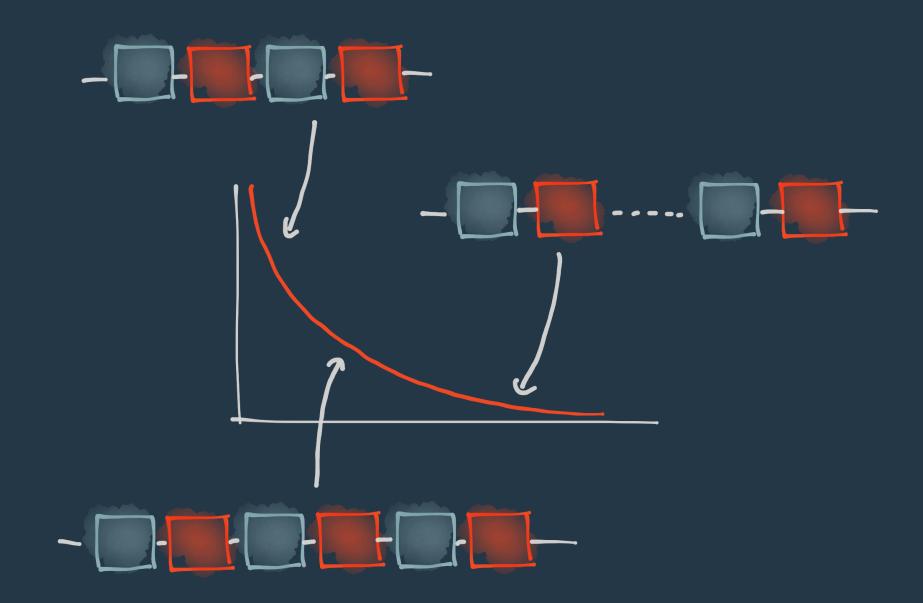
#### Algorithmic

- 1D transverse field Ising model simulation
- QAOA
- Repetition code
- HoloQUADS
- Logical memory
- QFT and Toffoli
- QED-C application oriented benchmarks
- Logical circuits
- Algorithmic qubits

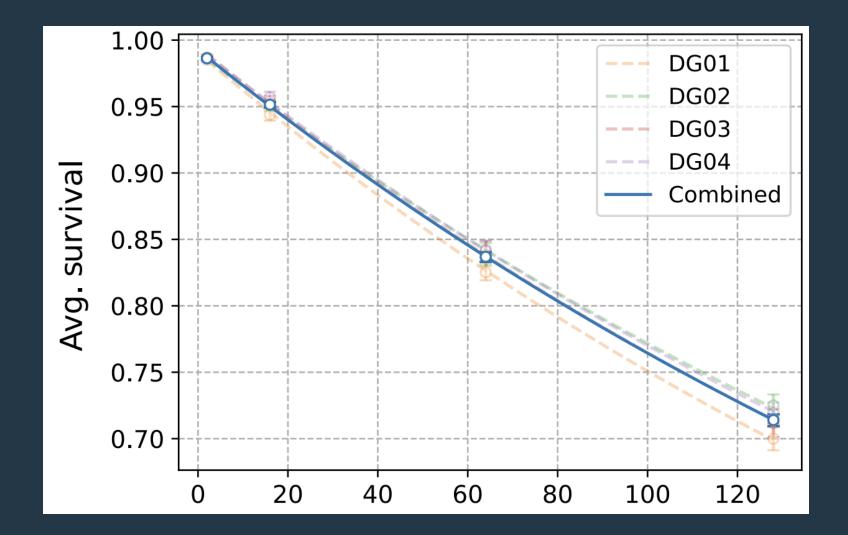
A Race-Track Trapped-Ion Quantum Processor https://journals.aps.org/prx/abstract/10.1103/PhysRevX.13.041052

## Randomised Benchmarking

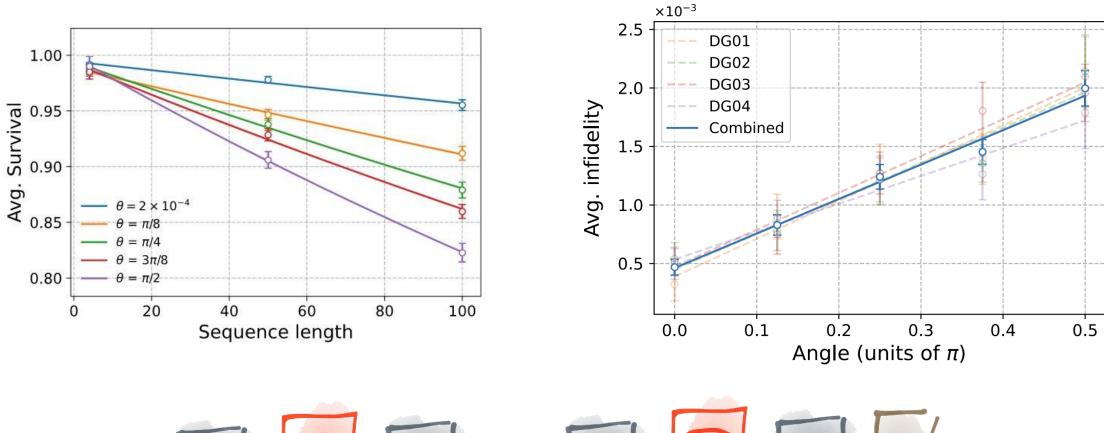
Component level benchmarking

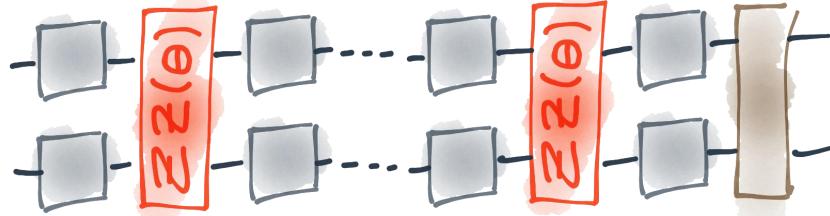


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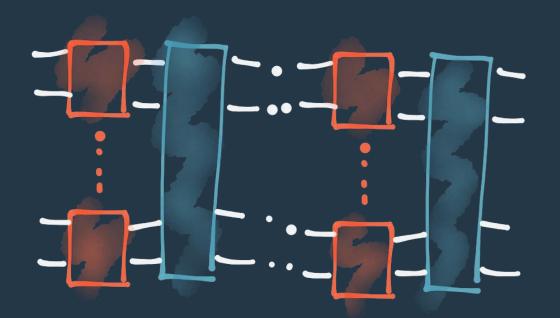
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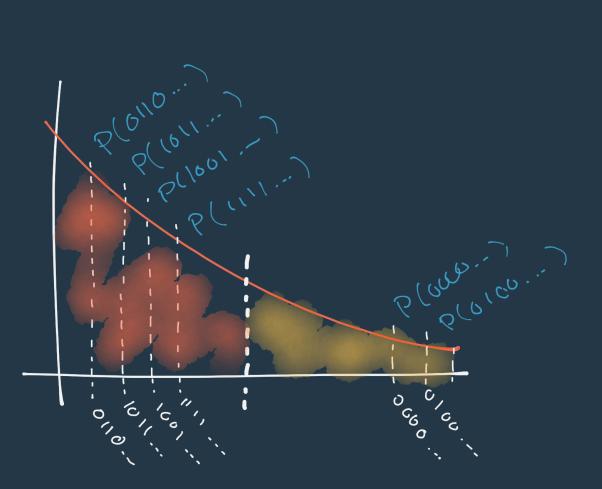


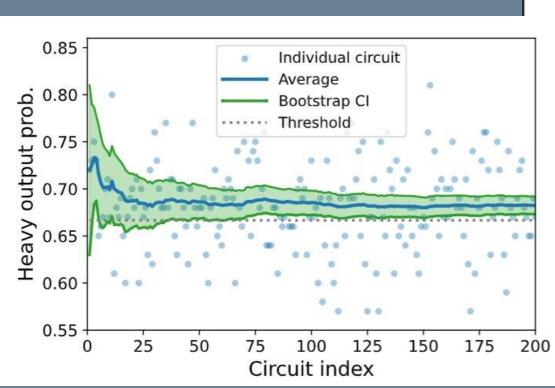


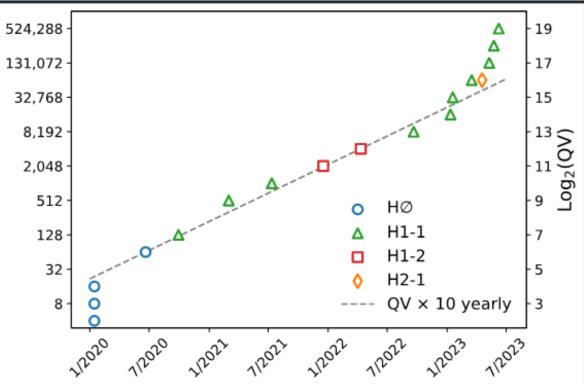
## Quantum Volume

System Level Benchmarking





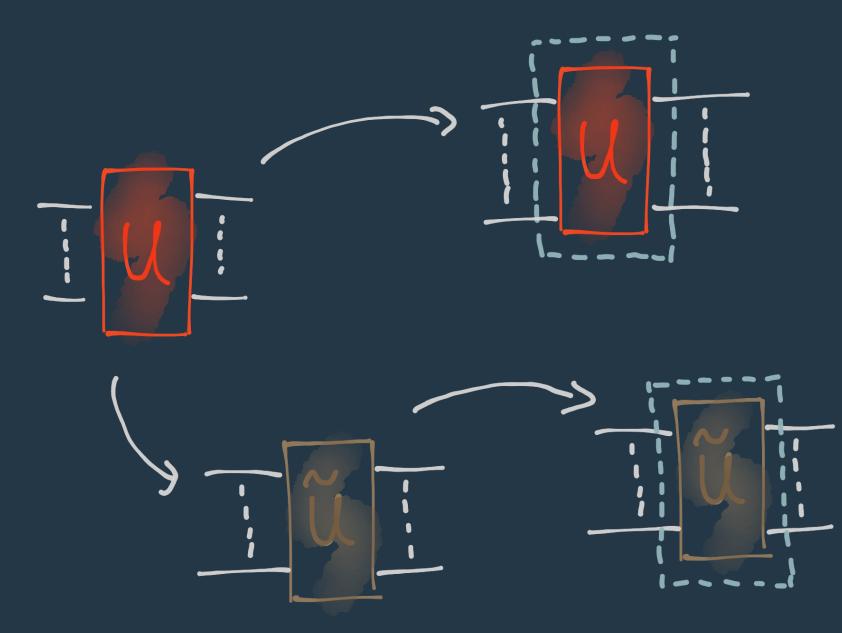




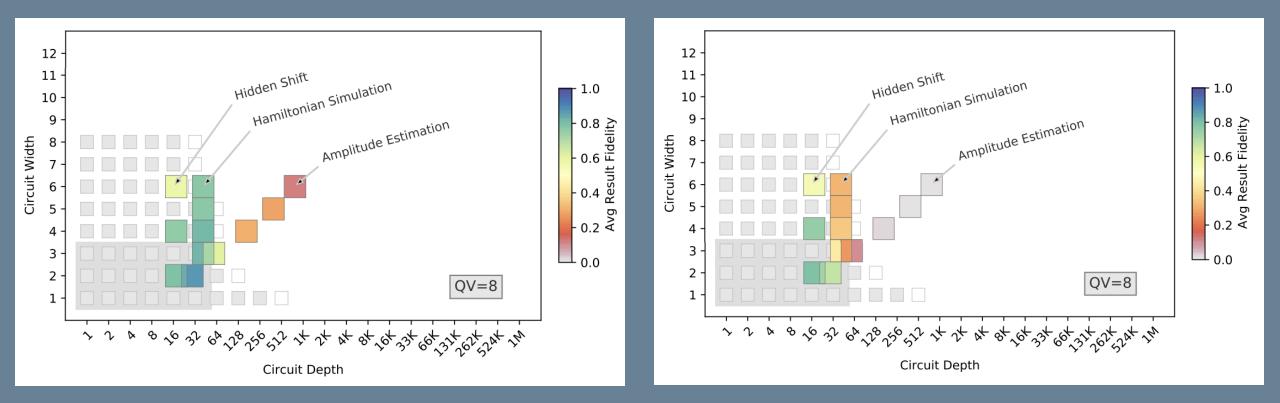
Quantum volume

## Compilers and the QED-C applicationoriented benchmarks

**Application-Level Benchmarks** 



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## **Closing Remarks**

Full stack (holistic) benchmarks are important.

 Not influenced by classical - unrepresentative.
 Perfected by perfecting layer - unrepresentative.

 Benchmarking error correction gadgets.

- Holistic error correction benchmarking.
- Standard implementation and reproducibility.

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