



## Hi, I'm Laurent 🎺

I'm a Product Manager at Alice & Bob

- Background in applied mathematics
- Product Manager for 13 years
- Setting up a cloud access to Alice & Bob's quantum computers



# Alice & Bob: building a universal fault-tolerant quantum computer





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PhD in Quantum Physics at ENS Graduated from École Polytechnique Expert in modular quantum architecture



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Co-founder & CTO

PhD in Quantum Physics at ENS
Graduated from ENS Ulm
Co-inventer of the cat qubit technology



Created in 2020

90 employees today (incl. 50 R&D)

30M€ raised in VC capital

>15 patents filed

>15 academic partnerships











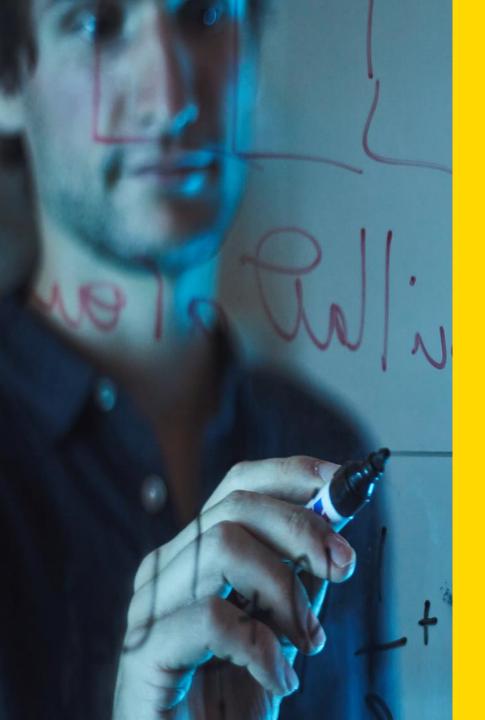










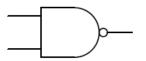


01

# The need for error correction

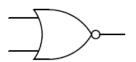
## Classical computing





#### **NAND**

A	В	Output
0	0	1
0	1	1
1	0	1
1	1	0



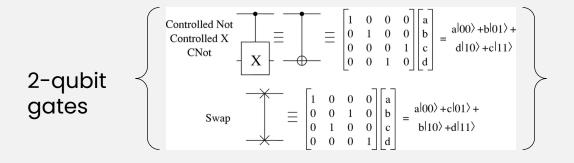
#### NOR

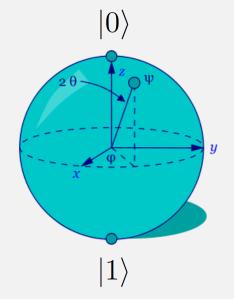
A	В	Output
0	0	1
0	1	0
1	0	0
1	1	0

# $\begin{array}{c} \text{X Gate} \\ \text{Bit-flip, Not} \end{array} \begin{array}{c} X \text{ Gate} \\ \text{Gate} \end{array} \begin{array}{c} X \text{ Gate} \\ \text{Phase-flip} \end{array} \begin{array}{c} X \text{ Gate} \\ \text{Color of the final of the$

Quantum

computing







## Quantum computers are SLOW!

We compare the peak performance of a single classical chip that can be manufactured today (like an NVIDIA A100 GPU, or an ASIC with a similar number of transistors) with a future quantum computer with 10,000 error-corrected logical qubits,  $10\mu s$  gate time for logical operations and all-to-all connectivity. We consider an estimate of the I/O bandwidth (namely the number of operations per second) and three types of operations: logical binary operations, 16-bit floating point, 32-bit integer or fixed-point arithmetic multiply add operations.

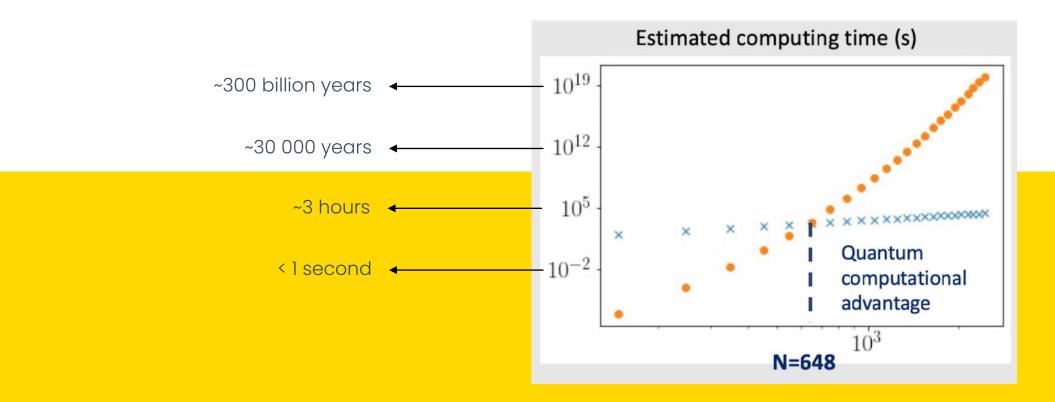
	GPU	ASIC	Future Quantum
I/O Bandwidth	10,000 Gbit/s	10,000 G/s	1 Gbit/s
Operation throughput			
16-bit floating point	195 Top/s	550 Top/s	10.5 kop/s
32-bit integer	9.75 Top/s	215 Top/s	0.83 kop/s
binary (Boolean logical)	4,992 Top/s	77,000 Top/s	235 kop/s

https://cacm.acm.org/research/disentangling-hype-from-practicality-on-realistically-achieving-quantum-advantage/



## Quantum advantage depends on problem size

**Example**: time required to find the prime factors of a N-bit integer





For example, the polynomial speedup of a quantum Monte Carlo is eaten away by differences in execution velocity.

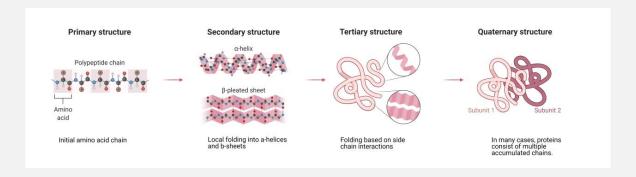
SUPERPOLYNOMIAL SPEEDUP	POLYNOMIAL SPEEDUP	
Quantum simulation for chemistry	Exponential congruence	
ODE and PDE	Subset sum	
Shor	Grover (search)	
Discrete-log	Constraint Satisfaction	



#### Modeling antibody loops on a quantum computer

Quantum Monte Carlo Roche & Tencent https://arxiv.org/pdf/2105.09690v1.pdf

	$n_{ m steps}$	$t_{ m step}$	Total time	Logical qubits
Classical [65]				-
Quantum (parallel)	$8.9 \times 10^{4}$	$16.4 \mathrm{\ mins}$	2.8  years	$\sim 4\times 10^4$
Quantum (serial)	$8.9 \times 10^{4}$	25.2  hours	256  years	$\sim 10^4$



# Exponential acceleration requires deep circuits

Running Shor's algorithm on an N bit key requires:

- O(N) qubits
- O(N³) circuit depth

These are  $O(N^4)$  opportunities to get an error

#### If:

- A step fails with probability p
- We want a 10% probability to get the right result

Then we want  $(1-p)^{N^4} > 10^{-1}$ .

RSA2048 p < 10<sup>-12</sup>

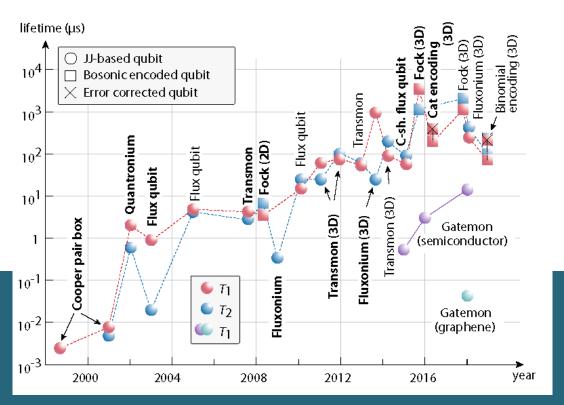
## Deep circuits require low error rates



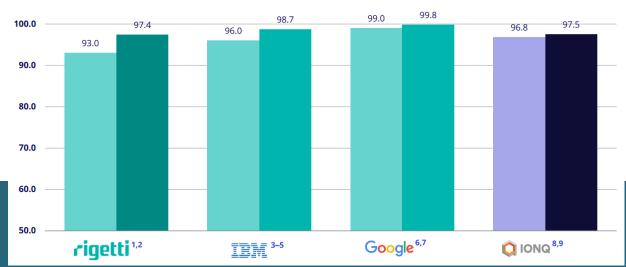




## Qubits are very far from that fidelity



#### Best demonstrated median 2Q fidelity: June 2017 vs. June 2021



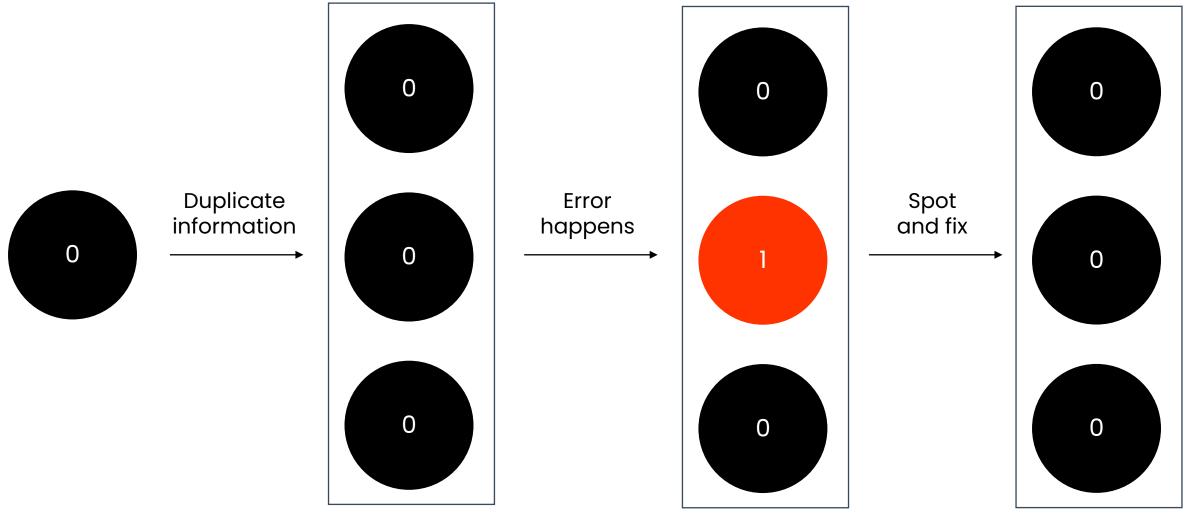
M. Kjaergaard, W.D. Oliver et al., Annual Review of Condensed Matter Physics, 2019.

Rigetti investor presentation October 2021



## So, we need error correction

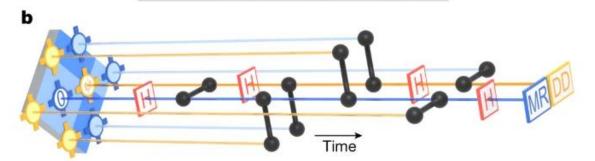
Or "fault-tolerance"



"Logical bit"



## And it's even harder in quantum computing



- You can't access the state of a qubit during computation
- You can't duplicate quantum information
- There are two types of errors to correct
- Error correction operations have a high probability of adding errors



# The road to a logical fault-tole rant quantum computer



#### 1. Physical qubits

- Create and stabilize qubits
- Perform physical gates





#### 2. Logical qubits (= fault-tolerance)

- Implement quantum error correction
- Improve hardware fidelity as required



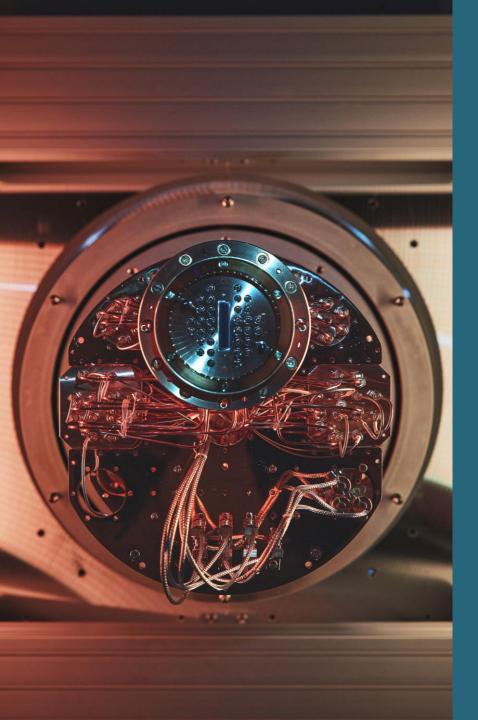


#### 3. Logical operations (= universality)

- Transversal gates
- Non-transerval gates (magic states)

Several years in the future





02

# Build physical qubits



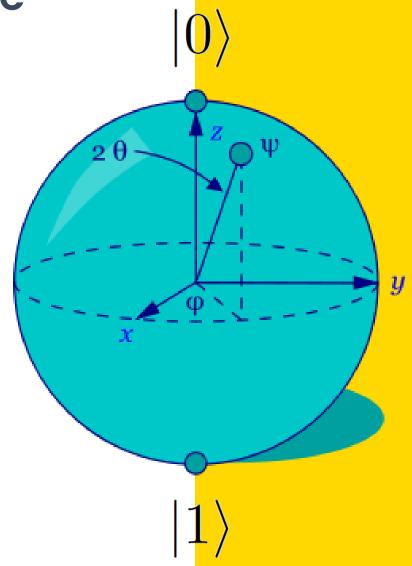
The cat qubit: Alice & Bob's choice

## Two types of errors:

- 1 Bit-flip (error on 0)
- **2** Phase-flip (error on φ)

## Cat qubits exhibit:

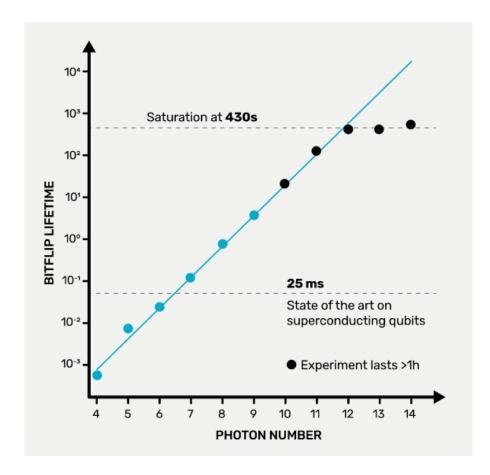
- 1 A lot fewer bit-flips (~106)\*
- 2 A little more phase-flips (~102)\*



<sup>\*:</sup> Boson 4 vs. a transmon with lifetime ~10-4 s



## The noise bias of cat qubits



 $T_1 = (6.82 \pm 0.24) \text{ us}$ 2.75

2.20

1.75

1.50

1.25

4 5 6 7 8 9 10

Number of photons

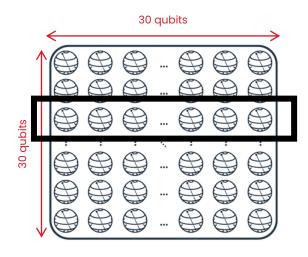
Bit-flip lifetime improves exponentially

Phase-flip lifetime decreases linearly

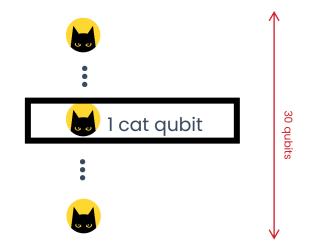
## Cat qubits

The perfect basis for logical qubits

#### **QUANTITATIVE APPROACH STANDARD QUBITS + SURFACE CODE**



#### **QUALITATIVE APPROACH CAT QUBITS + REPETITION CODE**



Shor to break RSA

20M physical qubits

VS

350k cat qubits

or

100k cat qubits using LDPC codes



E. Gouzien et al.





VS

Google

A&B cat qubit

Google physical qubits



VS



100% of the scientific articles cited

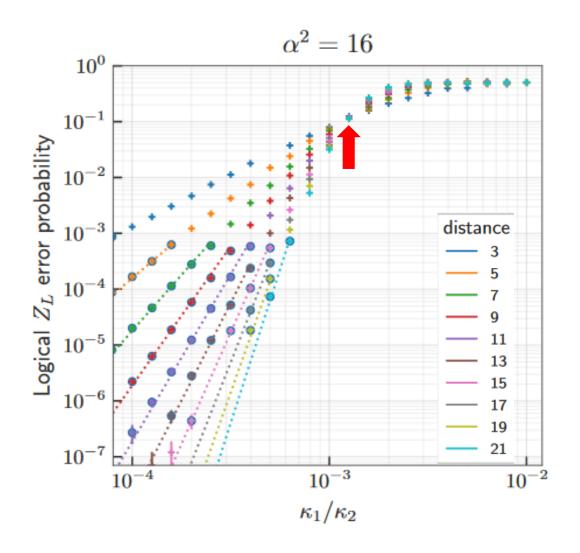
are from A&B

151x

mentions of A&B technology



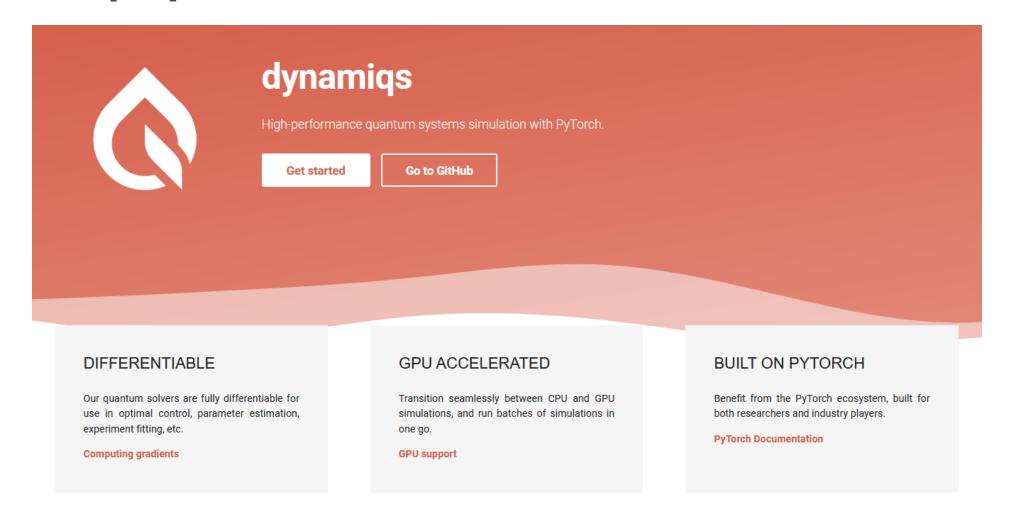
## Next goal: take qubits below the threshold



- Logical operations fidelity is mainly influenced by the underlying physical qubit quality
- There exists a magic point, called the threshold, below which the error rate of the logical operations decreases drastically (exponentially)
- We want to reach that point!







https://www.dynamiqs.org



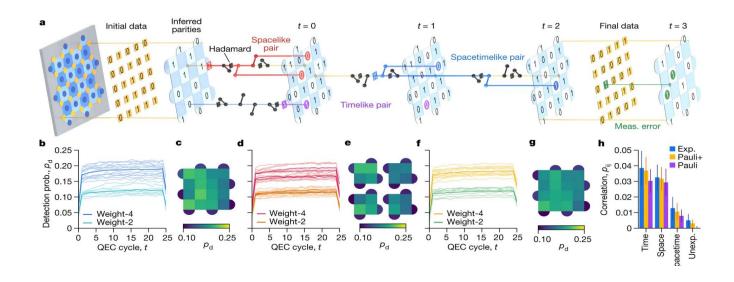


03

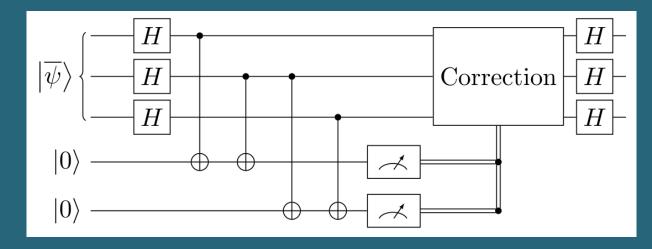
## **Build logical qubits**

## Error correction creates logical qubits

Biased vs. unbiased noise



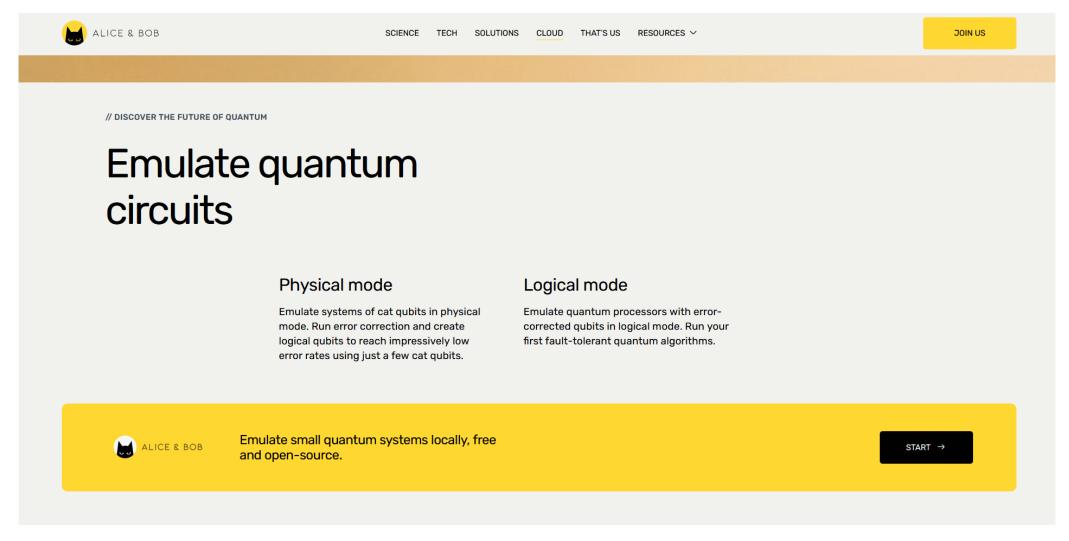
Unbiased noise:
Requires a complex 3D
surface code



Strongly biased noise: Requires only a 2D repetition code



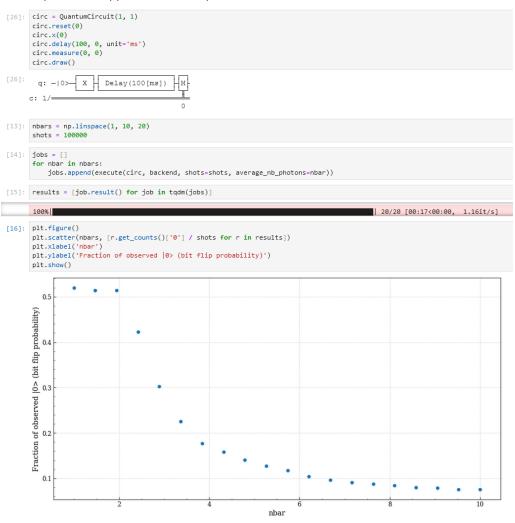






## Felis key features

#### Exponential suppression of bit flips



#### Access to real hardware

- Benchmark record-breaking cat qubits
- Reproduce Alice & Bob's experiments

### **Emulation capabilities**

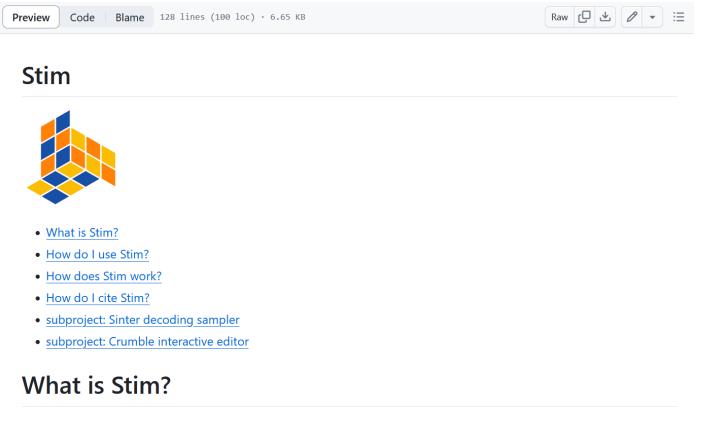
- Study error correction w/ physical backends
- Run algorithms w/ logical backends

### Supported by Qiskit

 Quantum computing's most popular framework





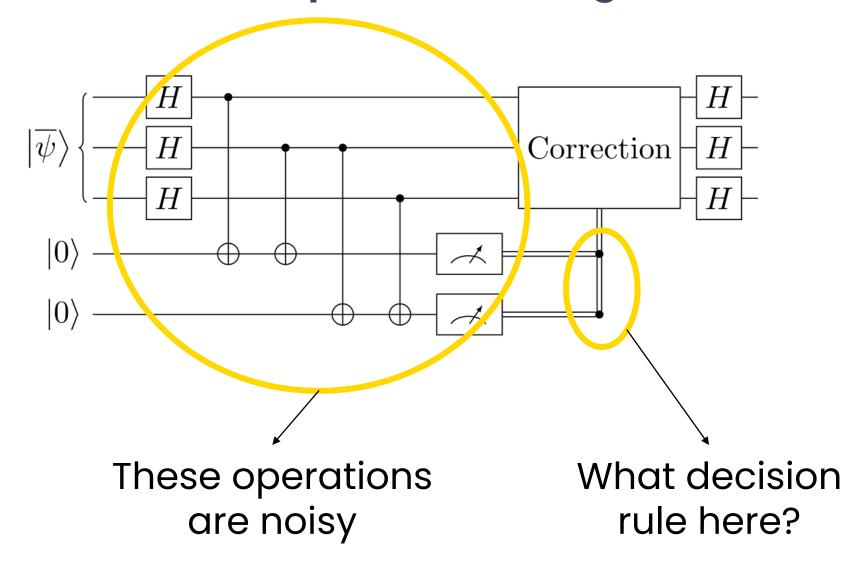


Stim is a tool for high performance simulation and analysis of quantum stabilizer circuits, especially quantum error correction (QEC) circuits. Typically Stim is used as a python package (pip install stim), though stim can also be used as a command line tool or a C++ library.

https://github.com/quantumlib/Stim/blob/main/README.md#how-cite-stim



## Measurements require decoding









PyMatching is a fast Python/C++ library for decoding quantum error correcting (QEC) codes using the Minimum Weight Perfect Matching (MWPM) decoder. Given the syndrome measurements from a quantum error correction circuit, the MWPM decoder finds the most probable set of errors, given the assumption that error mechanisms are *independent*, as well as *graphlike* (each error causes either one or two detection events). The MWPM decoder is the most popular decoder for decoding <u>surface codes</u>, and can also be used to decode various other code families, including <u>subsystem codes</u>, honeycomb codes and 2D hyperbolic codes.

Version 2 includes a new implementation of the blossom algorithm which is 100-1000x faster than previous versions of PyMatching. PyMatching can be configured using arbitrary weighted graphs, with or without a boundary, and can be combined with Craig Gidney's <u>Stim</u> library to simulate and decode error correction circuits in the presence of circuit-level noise. The <u>sinter</u> package combines Stim and PyMatching to perform fast, parallelised monte-carlo sampling of quantum error correction circuits.

Documentation for PyMatching can be found at: pymatching.readthedocs.io

https://github.com/oscarhiggott/PyMatching



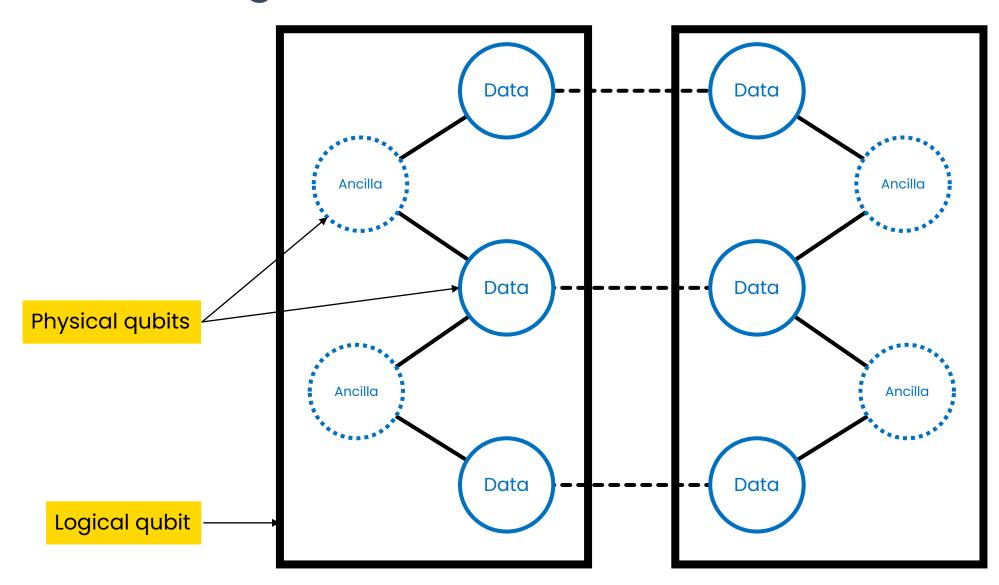


04

## **Build logical gates**



## A transversal gate

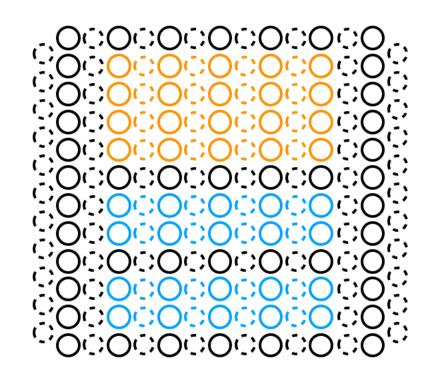




## But transversal gates aren't enough...

A 4-logical, 178-physical qubit processor

- O data qubit
- ancilla qubit
- Toffoli magic state factory qubit
- O Computational qubit
- O Routing qubit



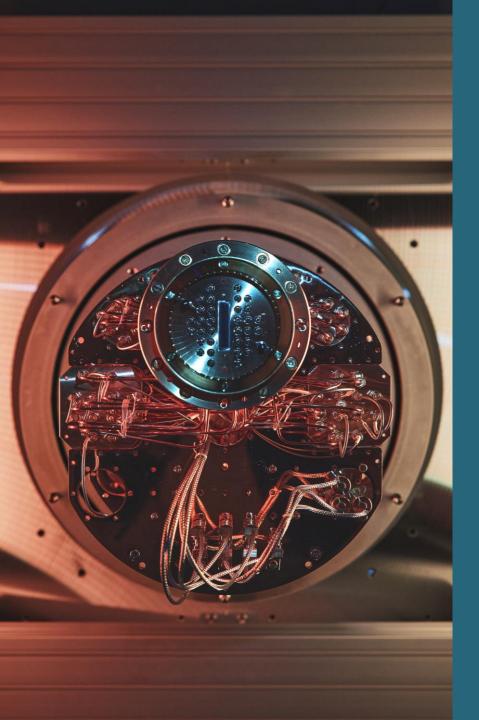
Compiling on
"logical" hardware

Compiling on
"physical" hardware

000000000

Logical qubit (distance-5)





05

## Use logical qubits

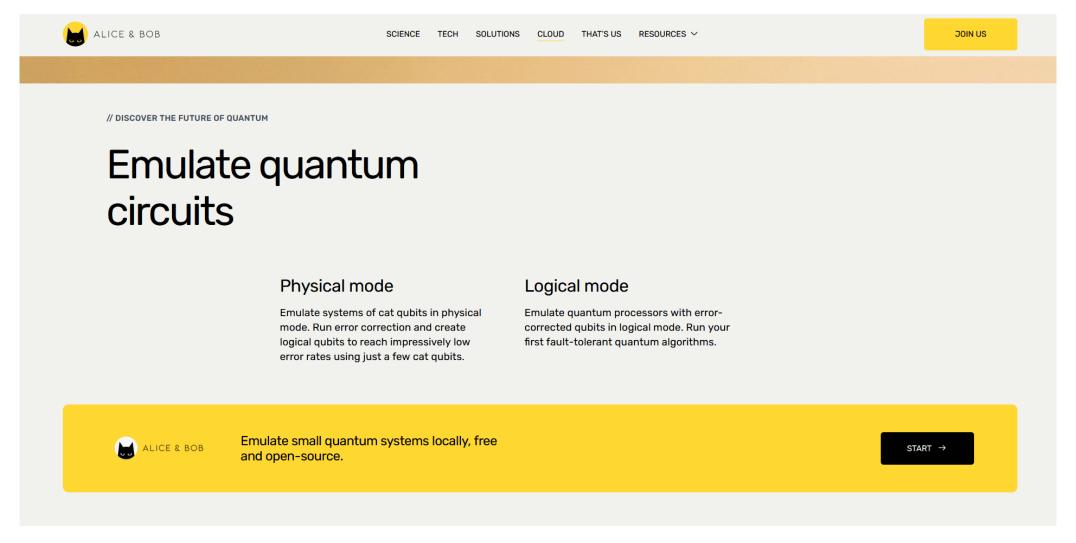


## New paradigm, new metrics

Metric	Physical qubits	Logical qubits
Error rate	Limited (probably > 10 <sup>-4</sup> or 10 <sup>-5</sup> )	Virtually arbitrarily good
Logical:physical ratio	1:1	15:1 to 3000:1
# of usable qubits	Limited by noise	Limited by # of physical qubits
Connectivity	Limited	All-to-all
Gate set	Continuous (incl. native parametrized rotations)	Discrete (Clifford + T)
Gate time	Fast (< 1 µs)	Slower (~20 µs)



## Tool #2 (again!): gate-level emulation





## Tool #5: quantum software development



Orisp is a high-level programming language for creating and compiling quantum algorithms. Its structured programming model enables scalable development and maintenance.



https://qrisp.eu/



https://www.classiq.io/

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## **Tool #6: resource estimation**

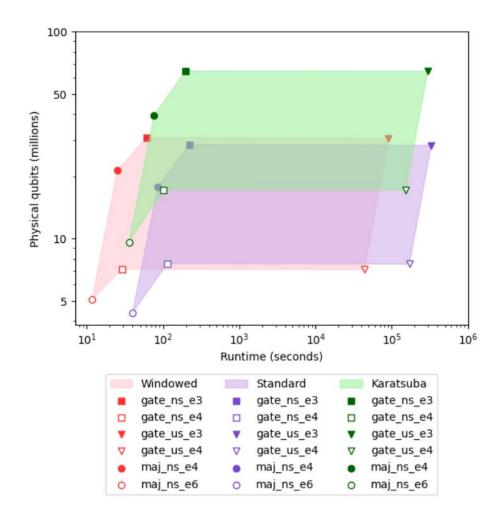
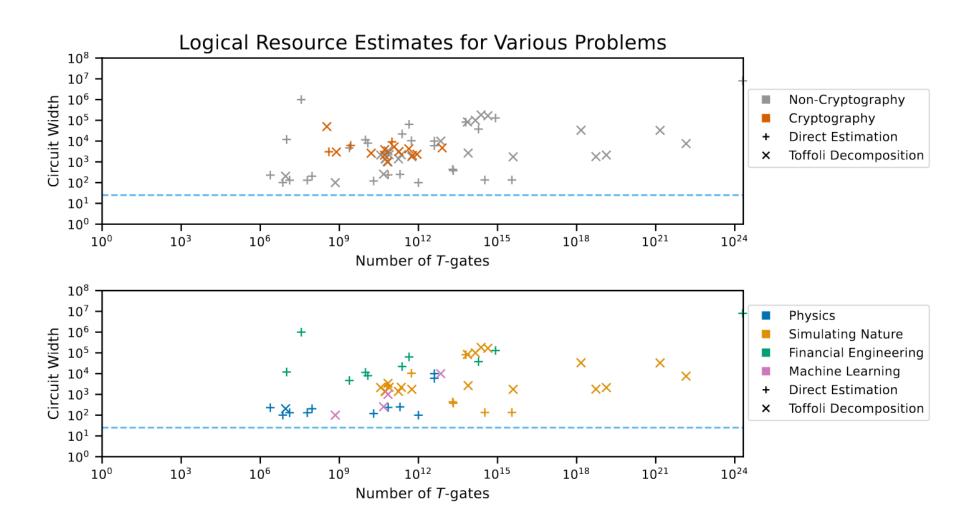


Fig. 4. The number of physical qubits and algorithm runtime across six different hardware profiles for the three multiplication algorithms, estimated for 2048-bit integers. The hardware profiles are described in Section III.C of [1]. The results are produced for total error budget 0.0001. The estimates for gate-based hardware profiles used the surface code QEC scheme with default parameters, and Majorana hardware profiles used floquet code QEC scheme with default parameters.



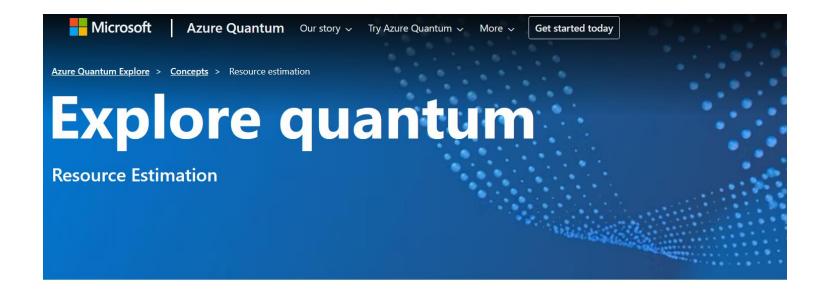
### **Tool #6: resource estimation**



https://arxiv.org/pdf/2401.16317



### **Tool #6: resource estimation**



Although quantum computers promise to solve some of the most intractable problems our society faces, such as climate change and food security, commercially viable solutions will require large-scale, fault-tolerant quantum computers. Solving these problems will also require algorithms and quantum applications capable of executing solutions at a scale, and in a timeframe, that is practical. But how do you know how long a given quantum application will take to run, or how many physical qubits it will require? In quantum computing, *Resource Estimation* is the process used to answer these questions. Resource Estimation can help you determine the number of qubits, quantum gates, processing time, and other resources needed to run a quantum program assuming specific hardware characteristics.

https://quantum.microsoft.com/en-us/explore/concepts/resource-estimation

## **Tool #6: resource estimation**



☐ README



### Q# resource estimator for Alice & Bob's architecture

This project contains the code for using Microsoft Q# resource estimator (presented in this paper) for Alice & Bob's architecture, using cat qubits and repetition code (LDPC codes might be added in the future).

Shor's algorithm for solving the elliptic curve discrete logarithm problem is used as an example, as in the paper <a href="Phys.">Phys.</a>
Rev. Lett. 131, 040602 (arXiv: 2302.06639). Results from the resource estimator can be compared with the one of <a href="theory: the code coming with the paper">the code coming with the paper</a>.

Big thanks to Mathias Soeken for having written the initial version of this repository, and rebuilt Microsoft Q# resource estimator to allow our architecture to be handled.

https://github.com/Alice-Bob-SW/qsharp-alice-bob-resource-estimator

