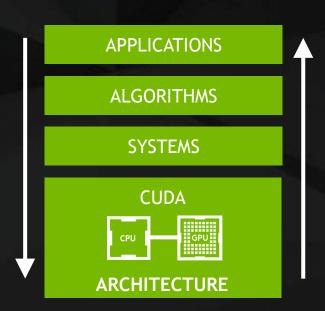
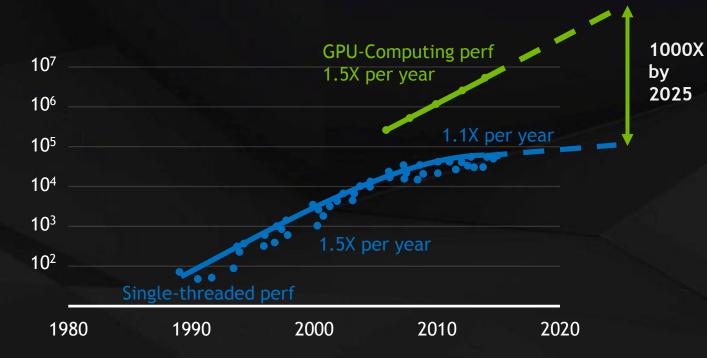


TERATEC 2017 UPDATE

Frédéric Parienté Tesla Accelerated Computing NVIDIA Corporation

THE RISE OF ACCELERATED COMPUTING





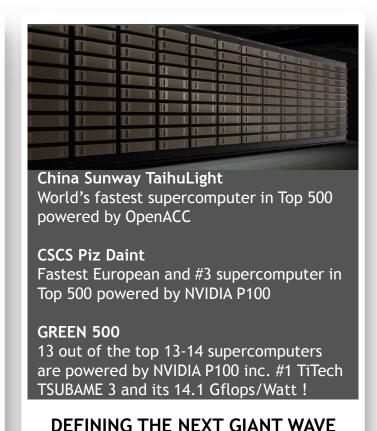
Original data up to the year 2010 collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond, and C. Batten New plot and data collected for 2010-2015 by K. Rupp

MOST ADOPTED PLATFORM FOR HPC



All Top 10 HPC Apps Accelerated Gaussian **ANSYS Fluent GROMACS** Simulia Abagus NAMD WRF VASP OpenFOAM LS-DYNA **AMBER**

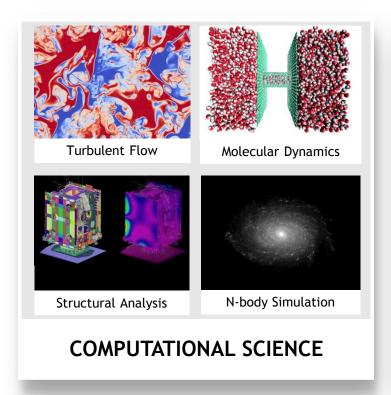
450+ GPU-ACCELERATED APPLICATIONS



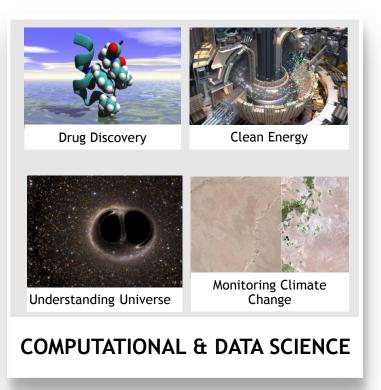
IN HPC

AI SUPERCOMPUTING WILL TRANSFORM HPC

Extending Reach of HPC By Combining Computational & Data Science







MOST ADOPTED PLATFORM FOR AI







TESLA V100

THE MOST ADVANCED DATA CENTER GPU EVER BUILT

5,120 CUDA cores 640 NEW Tensor cores 7.5 FP64 TFLOPS | 15 FP32 TFLOPS 120 Tensor TFLOPS 20MB SM RF | 16MB Cache | 16GB HBM2 @ 900 GB/s 300 GB/s NVLink



