



THE INTERNATIONAL MEETING FOR SIMULATION AND HPC

"Technology" or "application": 1st workshop zoom

Each year, the Teratec Forum—revealing emerging technologies in the fields of HPC, Big Data and Simulation—holds "technology" and "application" workshops. These informative sessions offer the perfect framework for learning about new application sectors such as Visualization and Multimedia; Digital Simulation and Health; Big Data and Agriculture; and HPC and Materials Engineering; etc.

1st zoom on four of the eight workshops to be held on Wednesday June 29:

■ **Workshop1 - Virtualization - 9am to 12.30pm**

Chaired by Guillaume COLIN DE VERDIERE, CEA

High performance computing (HPC) centres are increasingly turning to technologies used in the Cloud. Virtualization is one such technology that is on the rise, as the topics covered during this session will attest: [Workshop1](#)

■ **Workshop4 - Specialised Computing Architectures - 9am to 12.30pm**

Chaired by Gérard ROUCAIROL, TERATEC; Jean-Philippe NOMINE and Marc DURANTON, CEA

An explorative and forward-looking Workshop offering insight into different approaches in specialised computing architectures on various scales: [Workshop4](#)

■ **Workshop6 - High Performance Integrated Computational Materials Engineering - 2pm to 5.30pm**

Chaired by Gilles ZERAH, CEA

Material simulation codes are some of the biggest consumers of CPU cycles on high performance computers. The predictive abilities of simulations have given rise to the Integrated Computational Materials Engineering (ICME) movement. [Workshop6](#)

■ **Workshop7 - HPC Technologies & Industrial Internet of Things (IIoT) Infrastructures - 2pm to 5.30pm**

Chaired by Jacques DUYSSENS, ANSYS Inc.

Many new-generation systems and innovations now rely on a number of interconnected objects embedded with multiple sensors and organised into IIoT infrastructures. This Workshop will focus on the new high performance infrastructure and simulation technologies integrated into these infrastructures, while exploring specific applications: discover the benefits of the "smart sock"! [Workshop7](#)

And of course, this 11th edition of the Teratec Forum will also feature:

- [Plenary sessions](#) on Tuesday June 28, devoted to the technological challenges and diversity of uses of simulation and Big Data.

- **The exhibition**, 28 and 29 June, a trade fair of almost 80 stands, bringing together the key players in simulation and HPC.
- **The Digital Simulation Awards**: Six Digital Simulation Awards will be given in recognition of outstanding accomplishments in digital simulation, HPC, and big data analytics.
- **European Research Café**: An all new area dedicated to European research projects and initiatives in the fields of digital simulation, HPC, and Big Data. INRIA is a partner of the European Research Café (*Café Européen de la Recherche*).
- **Collaborative Project Area**: Bringing together twenty collaborative research projects approved by the Competitiveness Clusters ADVANCITY, ASTECH, CAP DIGITAL, SYSTEMATIC, and VEGEPOLYS.
- **SIMSEO will be at the TERATEC Forum to present the use of simulation in enterprises ranging from the very small (VSE) to small and medium (SME) and mid-sized businesses.** Throughout the two days of the Forum, educational sessions for VSEs, SMEs, and mid-sized business will be held, aimed at helping decision-makers identify the opportunities that simulation offers and weigh the industrial stakes.

REGISTER NOW: [Workshop registration](#)



Tuesday 28 & Wednesday 29 June 2016
Ecole Polytechnique, Palaiseau

Complete information online at: [Teratec Forum](#)

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