

CHAPI

Calcul embarqué Hautes performances pour les Applications Industrielles, petites & moyennes séries

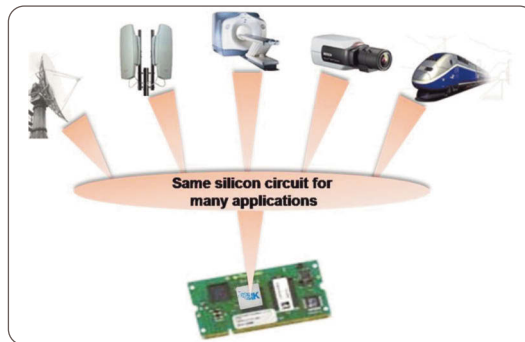


The collaborative project CHAPI supports the emergence of a new generation of programmable logic devices, which aims to serve the market demand in high performance & flexible integrated circuits for embedded computing. CHAPI will focus mainly on two topics:

- ▶ the optimization and validation of a first generation of circuits on use cases from several industrial application domains, and
- ▶ the integration of complementary software development tools to facilitate the development of new applications.

TECHNOLOGICAL OR SCIENTIFIC INNOVATIONS

- ▶ Prototyping of industrial applications using first generation of Kalray's MPPA™ technology in the domain of HD video encoding, image processing, transportation and industrial automation.
- ▶ Adaptation of a time-triggered operating system and tool chain for safety critical application support.
- ▶ Integration of high level parallel programming environments for easier application development.



STATUS - MAIN PROJECT OUTCOMES

The project began in January 2010. The first phase of the project includes the definition of the application cases for prototyping and support of the tools integration activities.

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CEA, DIGITEO/SCILAB,
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PROJECT DATA

Coordinator:
KALRAY

Co-label:
MINALOGIC

Call:
FUI8

Start date:
January 2010

Duration:
36 months

Global budget (M€):
12,4

Funding (M€):
5